

What is claimed is:

1. A low-capacitance laminate varistor comprising:

at least one pair of first and second inner electrodes;

a varistor layer, and

a first outer electrode and a second outer electrode, electrically connected to said first inner electrode and a second inner electrode, respectively;

wherein said first inner electrode and said second inner electrode are staggered so that although both are on the same plane, the electrode surfaces of both electrodes do not face each other.

2. The low-capacitance laminate varistor according to claim 1, wherein said first and second electrodes of said pair are separated by varistor layer and formed on different planes.

3. The low-capacitance laminate varistor according to claim 1, wherein said pair of first and second inner electrodes formed on one and the same plane of said varistor layer are in multiples.

4. The low-capacitance laminate varistor according to claim 2, wherein said pair of first and second inner electrodes formed on one and the same plane of said varistor layer are in multiples.

5. The low-capacitance laminate varistor according to claim 1, wherein the length of said pair of first and second inner electrodes is greater, equal to, or smaller than the width of varistor layer.

6. The laminate varistor according to claim 2, wherein the length of said pair of first and second inner electrodes is greater, equal to, or smaller than the width of varistor layer.

7. The laminate varistor according to claim 5, wherein the length of said pair of

first and second inner electrodes is greater, equal to, or smaller than the width of varistor layer.

8. The laminate varistor according to claim 6, wherein the length of said pair of first and second inner electrodes is greater, equal to, or smaller than the width of varistor layer.